



IEEE Computer
Society



TC on
Multiple-Valued Logic



JRG-MVL



MEIJI
University

33rd INTERNATIONAL SYMPOSIUM ON MULTIPLE-VALUED LOGIC

Meiji University, Tokyo, Japan
May 16-19, 2003

CALL FOR PAPERS

The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 33rd annual symposium on May 16-19, 2003 in Tokyo, Japan. The symposium is sponsored by the IEEE Computer Society, Japan Research Group on Multiple-Valued Logic and Meiji University. You are invited to submit an original paper, survey or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

- ALGEBRA AND FORMAL ASPECTS
- LOGIC DESIGN AND SWITCHING THEORY
- FAULT DETECTION AND DIAGNOSIS
- SPECTRAL TECHNIQUES
- CIRCUIT/DEVICE IMPLEMENTATION
- NANO TECHNOLOGY
- QUANTUM COMPUTING
- AUTOMATIC REASONING
- FUZZY LOGIC AND SOFT COMPUTING
- LOGIC PROGRAMMING
- DATA BASES AND DATA MINING
- RELIABILITY AND SAFETY
- COMPUTER SECURITY
- PHILOSOPHICAL ASPECTS

Authors should submit papers by **November 1, 2002**. Electronic submissions by PDF or PS files are strongly preferred. Though less preferred, submissions of four (double-spaced typed) paper copies are also acceptable. Each manuscript should include a 50-100 word abstract and should not exceed 8 pages in the proceedings format. Authors will be notified about acceptance by **February 1, 2003**. Photo-ready copies of accepted papers are due on **March 1, 2003**.

Submissions should be forwarded to one of the following **Program Co-Chairs**, choosing the closest among the specified fields of research:

THEORY, LOGIC DESIGN, FAULT DETECTION, PHILOSOPHY:

Prof. Radomir S. Stankovic, Dept. of Computer Science, Faculty of Electronics, University of Nis, Beogradska 14, 18000 Nis Serbia, Yugoslavia
e-mail: rstankovic@bankerinter.net FAX: 381 18 46-180

CIRCUITS, DEVICES, NANO TECHNOLOGY, QUANTUM COMPUTING, OTHERS:

Prof. Takao Waho, Department of Electrical and Electronics Engineering, Sophia University, 7-1 Kioicho, Chiyoda-ku, Tokyo 102-8554 Japan
e-mail: waho@sscd.ee.sophia.ac.jp FAX: 81-3-3238-4156

SYSTEMS, FUZZY LOGIC AND SOFT COMPUTING, SPECTRAL TECHNIQUES, APPLICATIONS:

Prof. Michael Miller, Department of Computer Science, University of Victoria, P.O. Box 3055, Victoria, BC, CANADA V8W 3P6
e-mail: mmiller@csr.csc.uvic.ca FAX: 1-250-721-8676

For additional information contact:

Prof. Masao Mukaidono, Symposium Chair

Department of Computer Science, Meiji University, Tama-ku, Kawasaki-shi 214-8571 Japan,
e-mail: masao@cs.meiji.ac.jp FAX.: 81-44-934-7912

The symposium will be complemented by the Post Binary ULSI workshop on 16 May 2003.